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1 Transactional events

Kevin Donnelly, Matthew Fluet

September 2006 ICFP '06: Proceedings of the eleventh ACM SIGPLAN inter programming

Publisher: ACM Pequest Permissions Full text available: Pof (264.80 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 43, Downloa

Concurrent programs require high-level abstractions in order to manage reasoning. In this paper, we introduce a novel concurrency abstraction, combines first-class synchronous messagepassing ...

Keywords: concurrency, first-class events, monads, synchronous mess

Also published in:

September 2006 SIGPLAN Notices Volume 41 Issue 9

2 Deadlock detection in distribution object systems

Nima Kaveh, Wolfgang Emmerich

September 2001 ESEC/ FSE-9: Proceedings of the 8th European software e ACM SIGSOFT international symposium on Foundations of

Publisher: ACM Request Permissions

Full text available: Pof (198.67 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 37, Downloa

The behaviour of a distributed system is largely determined by the use policies of the underlying middleware. The inherent parallel nature of diproblems, such as deadlocks ...

Keywords: UML, model checking, object middleware, process algebra

Also published in:

September 2001 SIGSOFT Software Engineering Notes Volume 26 Issue 5

3 Efficient fork-linearizable access to untrusted shared memory

Christian Cachin, Abhi Shelat, Alexander Shraer August 2007 PODC '07: Proceedings of the twenty-sixth annual ACM symp

Publisher: ACM Request Permissions
Full text available: Pdf (359.46 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 91, Downlo

When data is stored on a faulty server that is accessed concurrently by inconsistent data to different clients. For example, the server might cor respond with stale data ...

Keywords: arbitrary failures, fork-consistency, storage emulations

4 Checkpoint and Restart for Distributed Components in XCAT3
Sriram Krishnan, Dennis Gannon

November 2004 GRID '04: Proceedings of the 5th IEEE/ACM International'

Publisher: IEEE Computer Society Full text available: 120 (160.71 KB)

Additional Information: full citation, absti

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 22, Downloa

With the advent of Grid computing, more and more high-end computati scientist. While this opens up new avenues for scientific research, it mal system a non-trivial ...

Keywords: Grids, Components, Web Services, Distributed Checkpointir

⁵ Using elimination to implement scalable and lock-free FIFO queues

Mark Moir, Daniel Nussbaum, Ori Shalev, Nir Shavit
SPAA '05: Proceedings of the seventee

SPAA '05: Proceedings of the seventeenth annual ACM symp architectures

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 89, Downlo

This paper shows for the first time that elimination, a scaling technique structures, can be applied to FIFO data structures, specifically, to linear transform existing nonscalable ...

Keywords: FIFO queues, elimination, linearizability, lock-free, multipro scalability

6 Lazy argument passing in Java RMI

Christopher Line, K. R. Jayaram, Patrick Eugster

September 2008 **PPPJ '08:** Proceedings of the 6th international symposium in Java

Publisher: ACM

Full text available: Pdf (846.57 KB)

Additional Information: full citation, abstraction

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 67, Downloa

Though often criticized for its inherent synchronization overhead and co

paradigm remains one of the most popular abstractions for building dist suggested ways to ...

Keywords: Java, argument passing, asynchronous, future, invocation,

7 Inferential queueing and speculative push for reducing critical comm. Ravi Rajwar, Alain Kâgi, James R. Goodman

June 2003 ICS '03: Proceedings of the 17th annual international confere

Publisher: ACM ** Request Permissions Full text available: Pdf (568.93 KB)

Additional Information: full citation, abst:

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 28, Downloa

Communication latencies within critical sections constitute a major bottl workloads. In this paper, we argue for the use of Inferentially Queued L synchronization but also ...

Keywords: data forwarding, inferential queueing, synchronization

8 Architectural Support for Software Transactional Memory

Bratin Saha, Ali-Reza Adl-Tabatabai, Quinn Jacobson

December 2006 MICRO 39: Proceedings of the 39th Annual IEEE/ACM Inte

Publisher: IEEE Computer Society Full text available: Pdf (325.24 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 180, Downloads

Transactional memory provides a concurrency control mechanism that a synchronization. Researchers have proposed several different implemen classified into software transactional ...

9 Design tradeoffs in modern software transactional memory systems.

Virendra J. Marathe, William N. Scherer, Michael L. Scott

October 2004 LCR '04: Proceedings of the 7th workshop on Workshop on Ia for scalable systems

Publisher: ACM

Full text available: Pdf (158.62 KB) Additional Information: full citation, absti-

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 53, Downloa

Software Transactional Memory (STM) is a generic non-blocking synchro conversion of correct sequential objects into correct concurrent objects. traditional performance and ...

10 Software transactional memory for multicore embedded systems

Jennifer Mankin, David Kaeli, John Ardini June 2009

LCTES '09: Proceedings of the 2009 ACM SIGPLAN/SIGBED c tools for embedded systems

Publisher: ACM Request Permissions

Full text available: Pdf (443.92 KB) Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 41, Downloads (12 Months): 167, Downloads

Embedded systems, like general-purpose systems, can benefit from par platform. Unfortunately, concurrency issues present in general-purpose systems, protection from which is currently ...

Keywords: embedded systems, locking, multicore, software transaction transactions

Also published in:

June 2009 SIGPLAN Notices Volume 44 Issue 7

11 Constant-RMR implementations of CAS and other synchronization p Wojciech Golab, Vassos Hadzilacos, Danny Hendler, Philipp Woelfel

August 2007 PODC '07: Proceedings of the twenty-sixth annual ACM symp

Publisher: ACM Request Permissions

Full text available: Pof (289.54 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 31, Downloa

We consider asynchronous multiprocessors where processes communicate memory. We show how to implement consensus, all comparison primitive linked/store-conditional using only a constant ...

Keywords: comparison primitives, consensus, mutual exclusion, remot

12 Performance Evaluation of Task Pools Based on Hardware Synchrol Ralf Hoffmann, Matthias Korch, Thomas Rauber

November 2004 SC '04: Proceedings of the 2004 ACM/IEEE conference on § **Publisher:** IEEE Computer Society

Full text available: Pdf (208.41 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 35, Downlo

A task-based execution provides a universal approach to dynamic load k are arbitrary units of work that are created dynamically at run-time and the task pool, until ...

13 Robust implicit EDF: A wireless MAC protocol for collaborative real-t Tanya L. Crenshaw, Spencer Hoke, Ajay Tirumala, Marco Caccamo

September 2007 Transactions on Embedded Computing Systems (TEC

Publisher: ACM Request Permissions

Full text available: Pdf (663.47 KB) Additional Information: full citation, abst:

Bibliometrics: Downloads (6 Weeks): 19, Downloads (12 Months): 111, Downl

Advances in wireless technology have brought us closer to extensive de systems connected through a wireless channel. The medium-access con providing a real-time guarantee. ...

Keywords: Earliest deadline first, medium-access control, real time, wi

14

Deriving linearizable fine-grained concurrent objects
Martin Vechev, Eran Yahav

June 2008 PLDI '08: Proceedings of the 2008 ACM SIGPLAN conference

implementation **Publisher:** ACM Request Permissions

Full text available: Pdf (935.82 KB)

Additional Information: full citation, abstraction

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 140, Downl

Practical and efficient algorithms for concurrent data structures are difficult the literature are often optimized for a specific setting, making it hard to implementation ...

Keywords: algorithms, concurrency, data structures, linearizability, mc

Also published in:

May 2008 SIGPLAN Notices Volume 43 Issue 6

15 Aggressive snoop reduction for synchronized producer-consumer co

embedded multi-processors Chenjie Yu, Peter Peter Petrov

September 2007 **CODES+ISSS '07:** Proceedings of the 5th IEEE/ACM inter codesign and system synthesis

Publisher: ACM

Full text available: Fdf (163.62 KB)

Additional Information: full citation, abstraction

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 41, Downloa

Snoop-based cache coherence protocols are typically used when multipl common bus. It is well known, however, that these coherence protocols help alleviate this problem, ...

Keywords: cache coherence, embedded systems, low-power multiproc

16 Nonblocking k-compare-single-swap

Victor Luchangco, Mark Moir, Nir Shavit

June 2003 SPAA '03: Proceedings of the fifteenth annual ACM symposiu

Publisher: ACM Pequest Permissions
Full text available: Pdf (197.17 KB)

Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 41, Downloa

The current literature o .ers two extremes of nonblocking software sync structure design:intricate designs of specific structures based on single-swap (CAS), and general-purpose ...

Keywords: concurrent data structures, linked lists, multiprocessors, no freedom

17 Practice of parallelizing network applications on multi-core architectu Junchang Wang, Haipeng Cheng, Bei Hua, Xinan Tang June 2009 ICS '09: Proceedings of the 23rd international conference on Publisher: ACM Pequest Permissions

Full text available: Pdf (1.05 MB) Additional Information: full citation, abstr

Bibliometrics: Downloads (6 Weeks): 58, Downloads (12 Months): 154, Downloads

The industry wide shift to multi-core architectures arouses great interes However, it is very difficult to parallelize fine-grained applications for mi hardware support ...

Keywords: application-level protocol processing, deep content inspecti parallelization, pipelining implementation, tcp/ip protocol processing

18 Parallelizing security checks on commodity hardware

Edmund B. Nightingale, Daniel Peek, Peter M. Chen, Jason Flinn

March 2008 ASPLOS XIII: Proceedings of the 13th international conferen programming languages and operating systems

Publisher: ACM Pequest Permissions

Full text available: Elv (26:0 MIN), Pdf (306.87 KB) Additional Information: full citatio

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 194, Downloads

Speck (Speculative Parallel Check) is a system that accelerates powerful executing them in parallel on multiple cores. Speck provides an infrastru particular ...

Keywords: operating systems, parallel, performance, security, specula

Also published in:

March 2008 SIGPLAN Notices Volume 43 Issue 3 March 2008 SIGARCH Computer Architecture News Volume 36 Issue 1 March 2008 SIGOPS Operating Systems Review Volume 42 Issue 2

19 Data sharing protocols for SMT processors

Shi-Wu Lo

April 2006 SAC '06: Proceedings of the 2006 ACM symposium on Applied

Publisher: ACM Request Permissions

Full text available: Pdi (197.60 KB) Additional Information: full citation, absti-

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 18, Downloa

Although there are many real-time task synchronization protocols desig systems, most of them do not fit the needs in accommodating simultane synchronization protocols are ...

Keywords: real-time, scheduling, simultaneously multithreading

20 Improving instruction cache performance in OLTP.

Stavros Harizopoulos, Anastassia Ailamaki

September 2006 Transactions on Database Systems (TODS), Volume 31

Publisher: ACM Request Permissions Full text available: Pdf (783.16 KB)

Additional Information: full citation, absti

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 105, Downloads

Instruction-cache misses account for up to 40% of execution time database workloads. In contrast to data cache misses, instruction misse execution. Chip design limitations ...

Keywords: Instruction cache, cache misses

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